

## ACEMP-OPTIM25-0000080, Partha De



**Partha De** received the M.S. degree in Computer Science and Engineering from the Indian Institute of Technology (IIT) Kharagpur, India, and the Ph.D. degree in Computing and Information Systems from the University of Melbourne, Australia.

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His research interests include side-channel attack-resistant hardware design, pre-silicon leakage modeling, cryptography, low-power VLSI design, high-level synthesis, and the implementation of post-quantum cryptographic algorithms