

# Welcome to tutorial on:

## Digital Electronic System on Chip Design Methodologies – Evolution and Trends

A professional headshot of a man with short, dark hair and a light beard. He is wearing a dark suit jacket, a white shirt, and a patterned tie. He is looking directly at the camera with a slight smile.

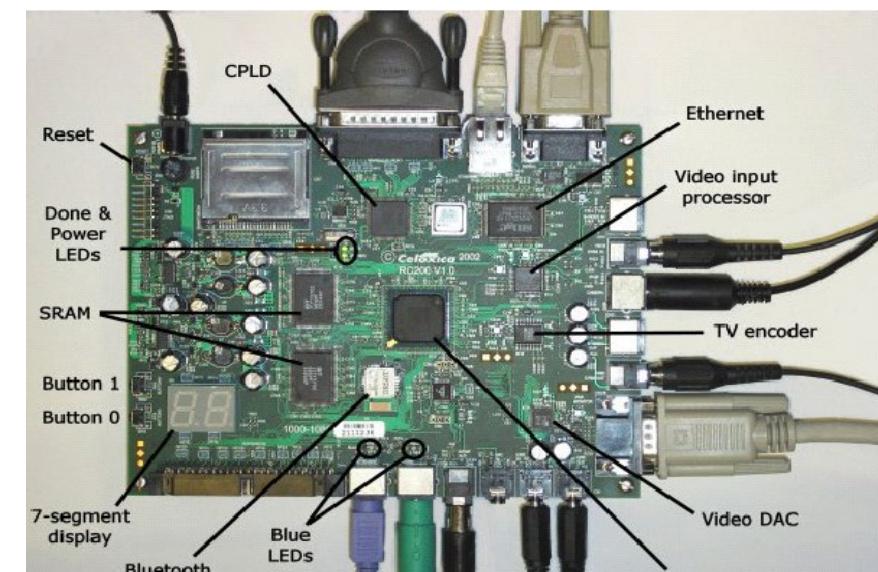
**Professor Marcian Cirstea**  
**Head of School**

**School of Computing and Information Science**  
**Anglia Ruskin University (ARU)**  
**Cambridge, UK**



# Outline of tutorial / presentation

- Speaker's background: 10 min
- Introduction of Anglia Ruskin University (ARU), UK: 20 min
- Tutorial part one: Digital SoC Design Methodologies – *Evolution*: 70 min
- Coffee break: 30 min
- Tutorial part two: Digital SoC Design Methodologies – *Trends*: 50 min



## Main reference (open access):

Cirstea, M., Benkrid, K., Dinu, A., Ghiriti, R., Petreus, D. "Digital Electronic System-on-Chip Design: Methodologies, Tools, Evolution, and Trends." MDPI Micromachines, 2024, 15, 247. <https://doi.org/10.3390/mi15020247>

